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<p>SCANNED <i>RW</i> O.I.P.E. <i>over</i> O.A. <i>PH</i></p>										<p>PATENT DATE</p>																			
<p>APPLICATION NO. 09/671800</p>					<p>CONT/PRIOR D</p>					<p>CLASS 451</p>					<p>SUBCLASS 4L</p>					<p>ART UNIT 3723</p>					<p>EXAMINER <i>DA NGUYEN</i> <i>BANKS</i> <i>prover</i></p>				
<p>APPLICANTS</p> <p>Bulent Basol Cyprian Uzoh Homayoun Talieh</p>																													
<p>TITLE</p> <p>Process to minimize and/or eliminate conductive material coating over the top surface of a patterned substrate and layer structure made thereby</p>																													
<p align="right">PTO-2040 12/89</p>																													

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ _____ _____ (Primary Examiner) _____ (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Legal Instruments Examiner) _____ (Date)		ISSUE BATCH NUMBER	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				

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